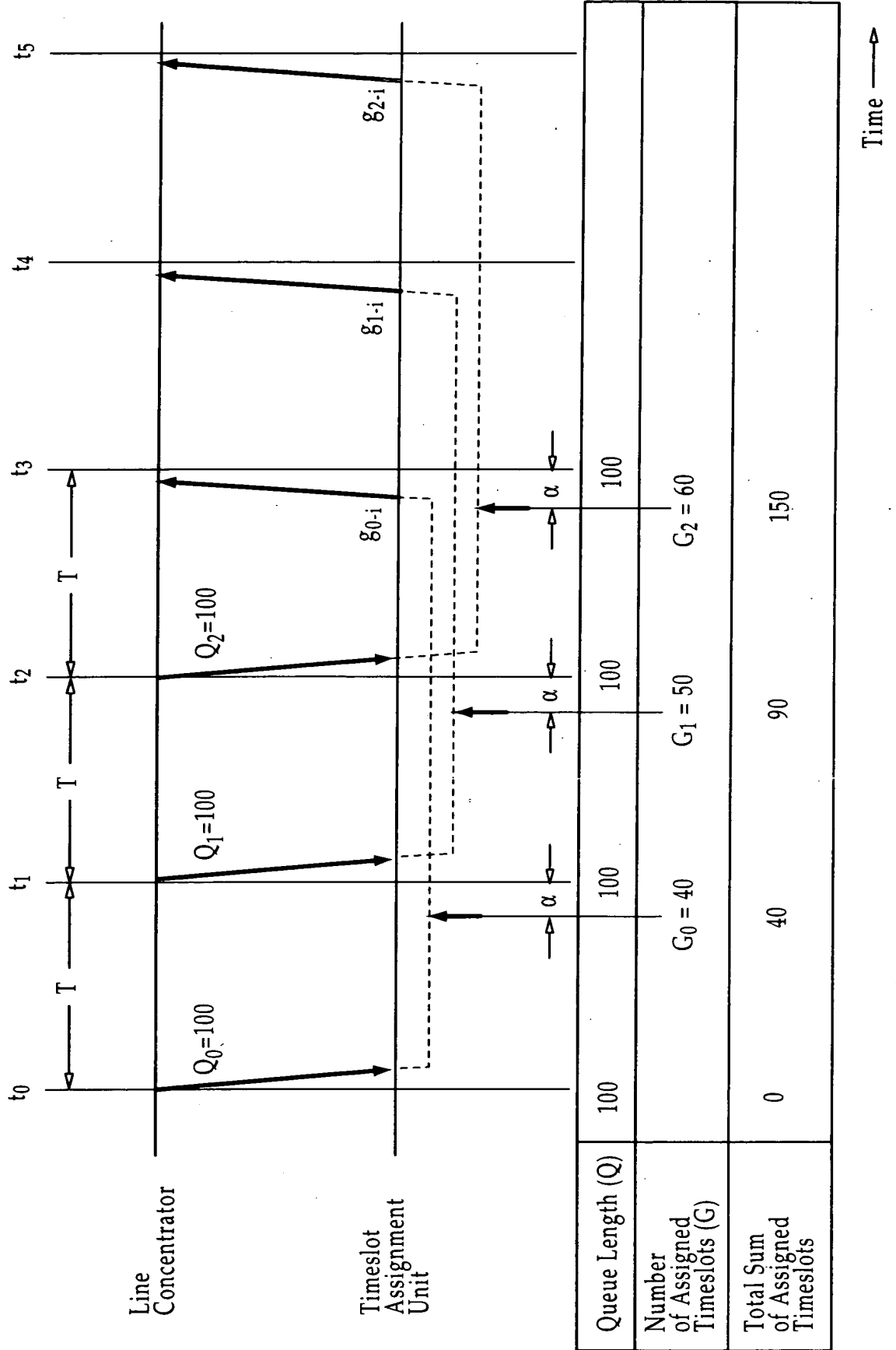


FIG. 1
Prior Art



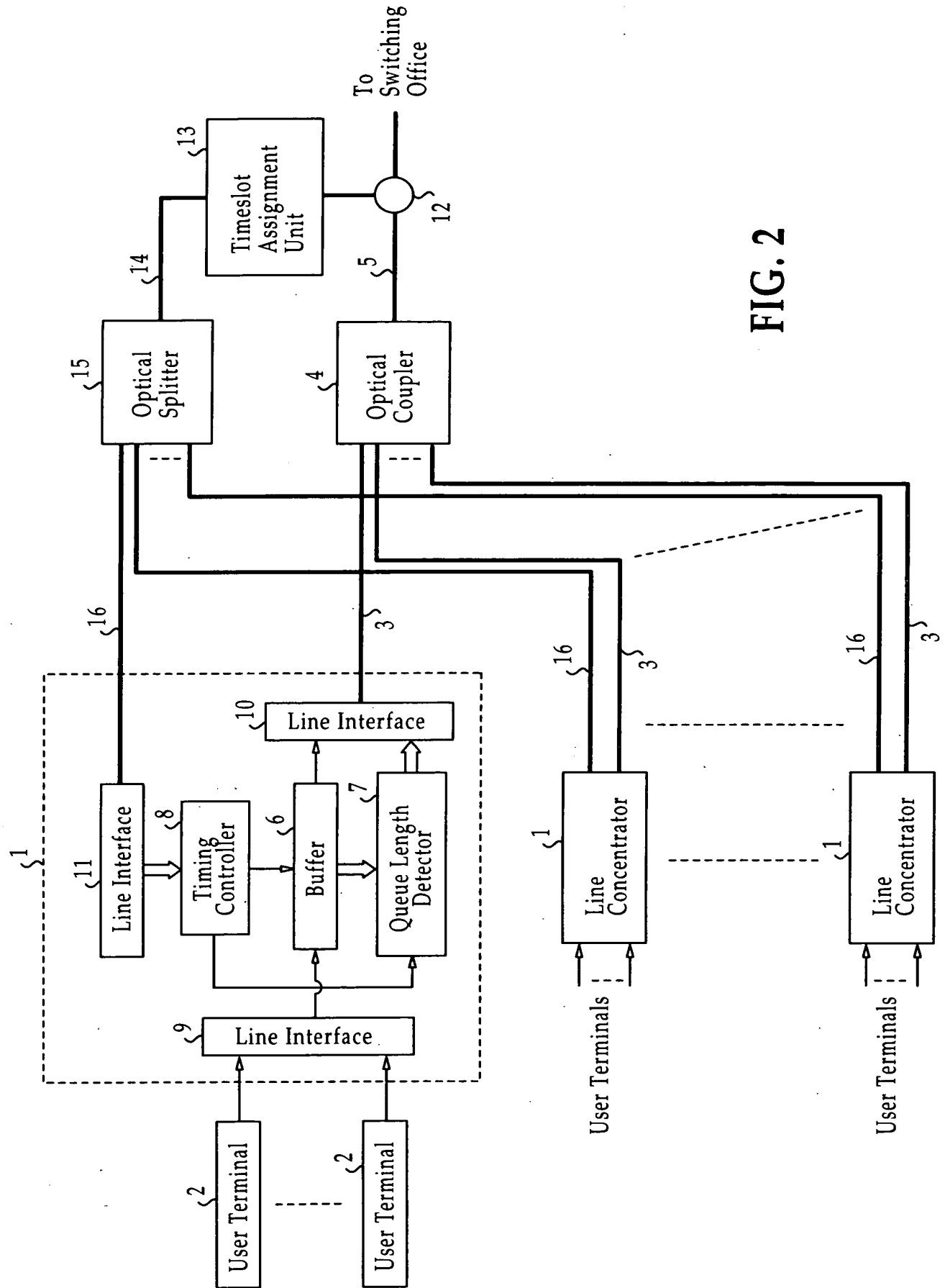


FIG. 2

FIG. 3

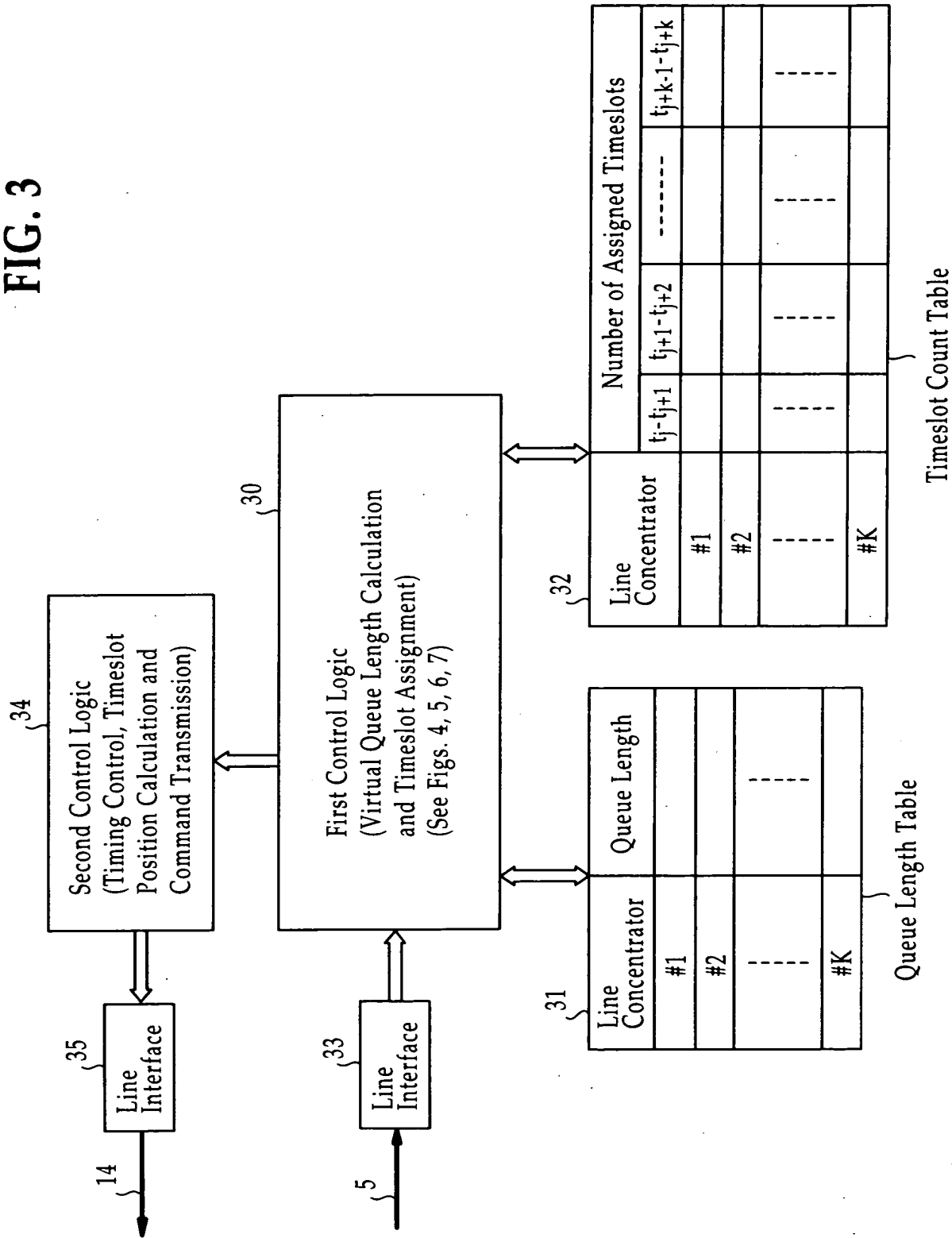


FIG. 4

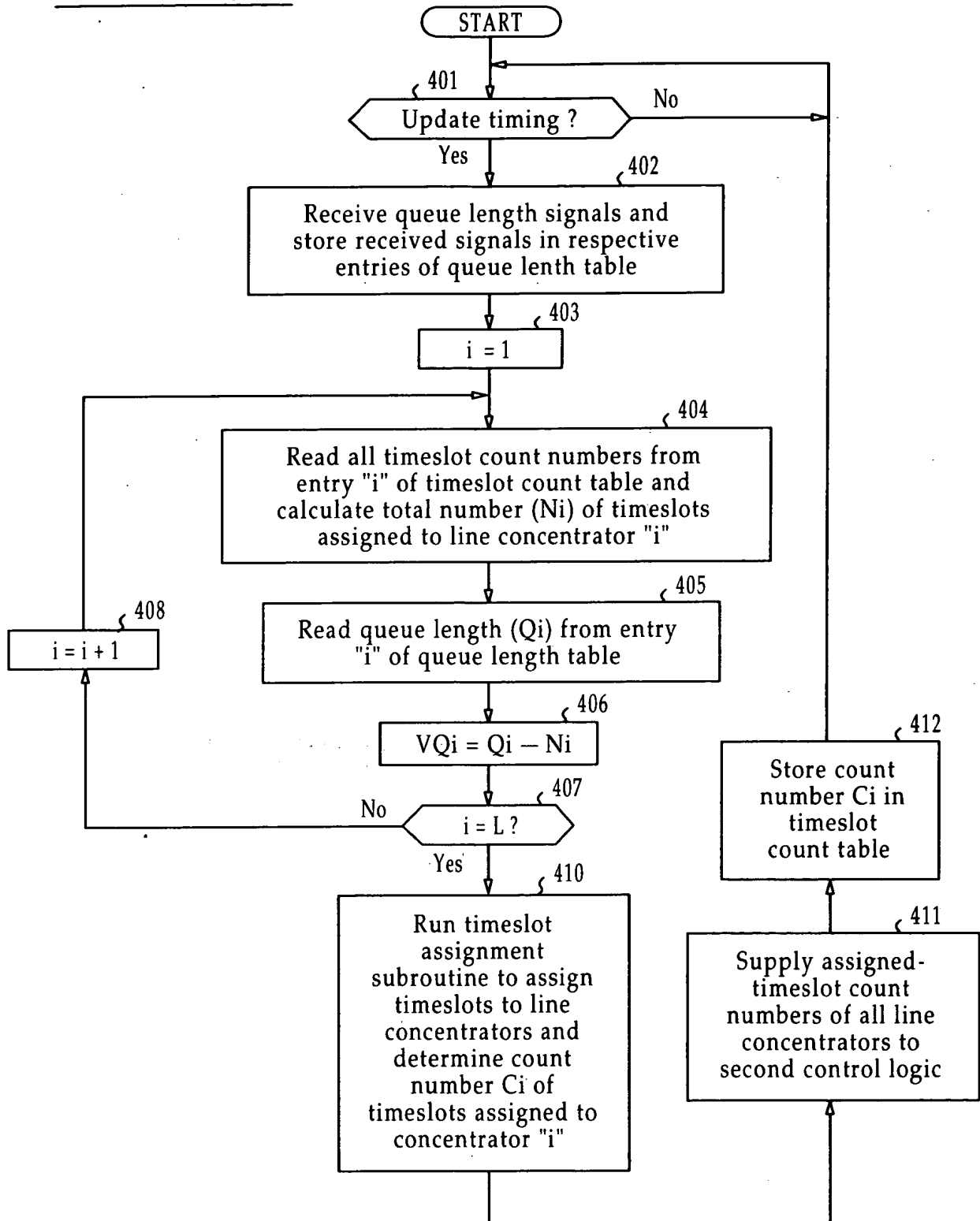
First Control Logic 30

FIG. 5

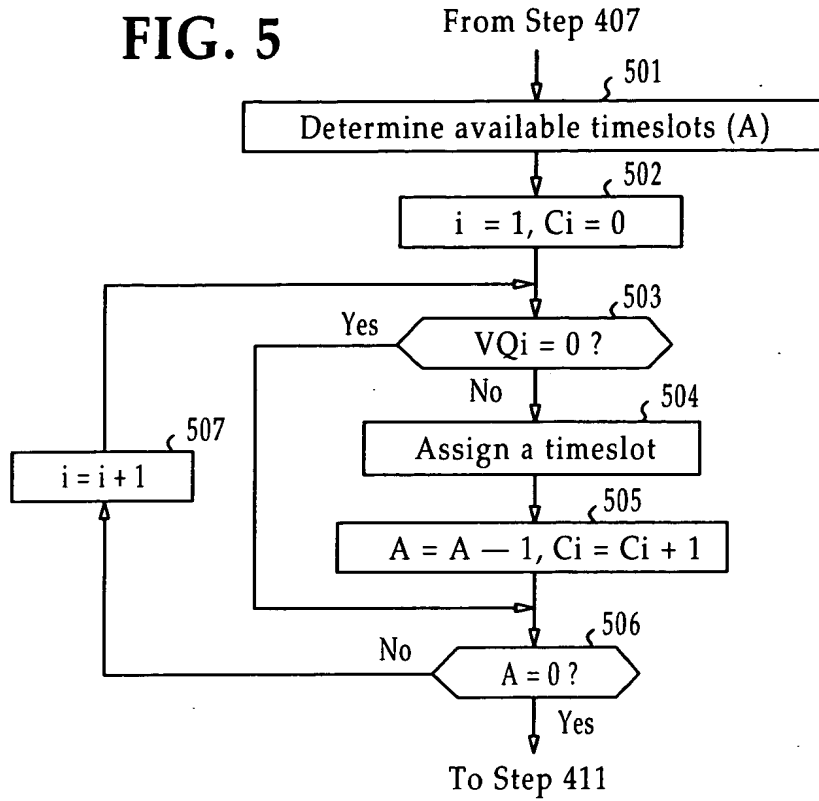


FIG. 6

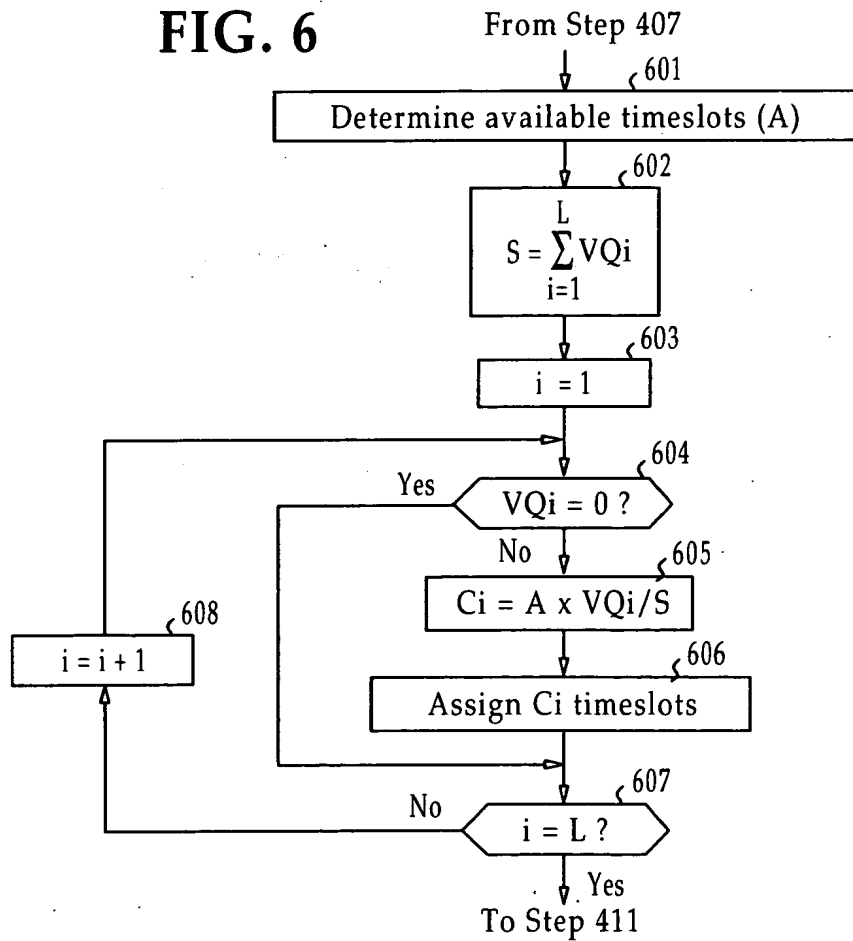


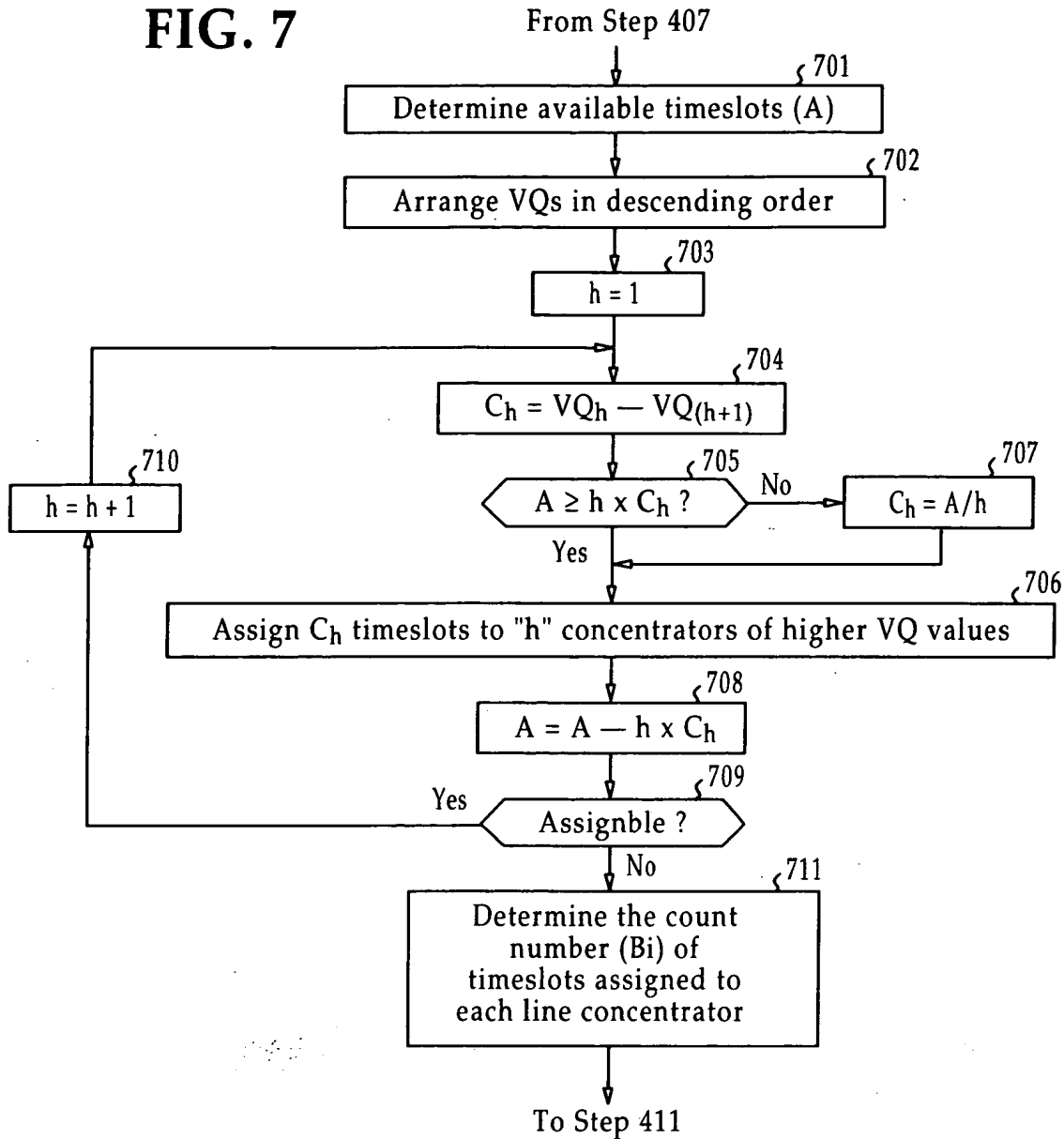
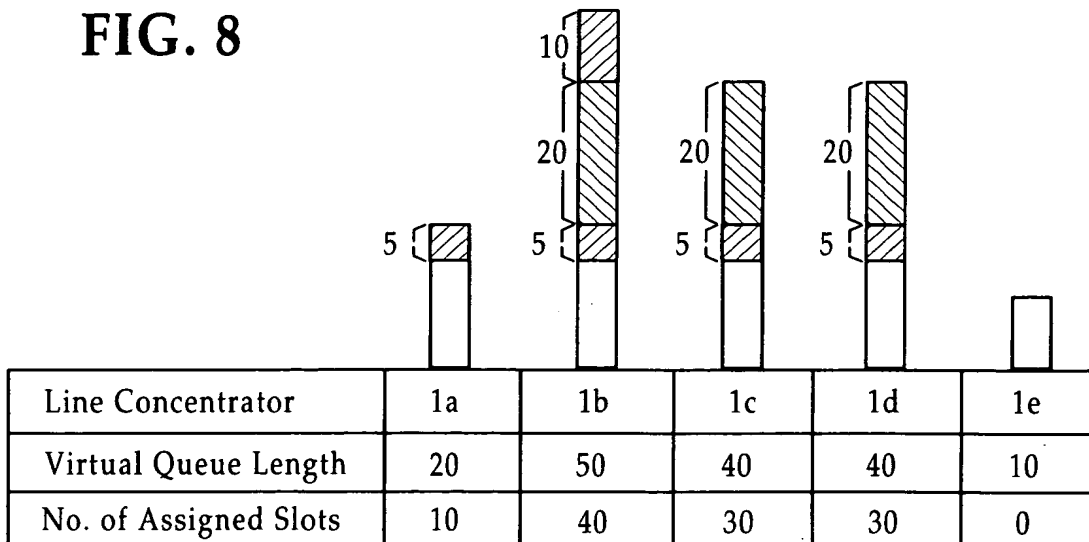
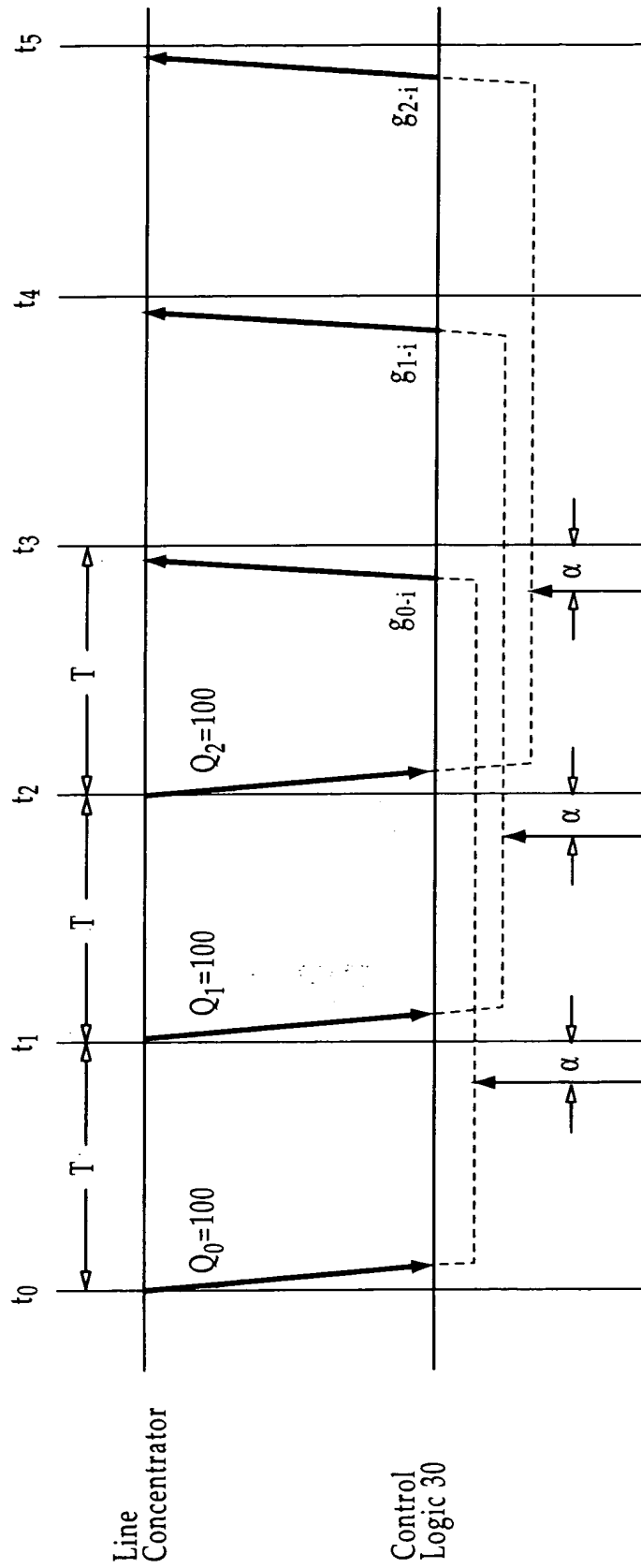
FIG. 7**FIG. 8**

FIG. 9



Queue Length (Q)	100	100	100	100
Virtual Queue Length	100	60	25	0
Number of Assigned Timeslots	0	40	35	25
Total Sum of Assigned Timeslots	0	40	75	100

FIG. 10

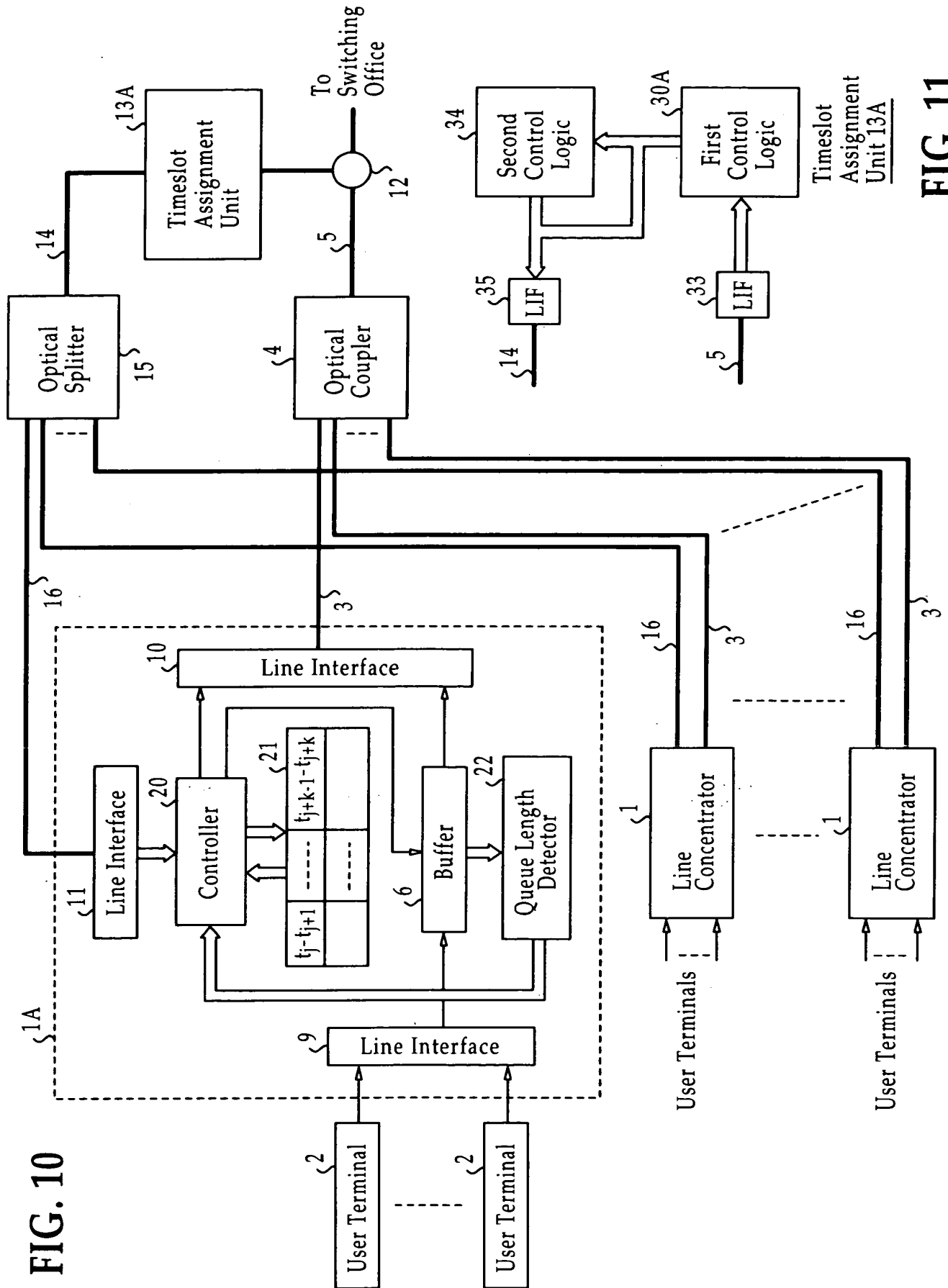


FIG. 11

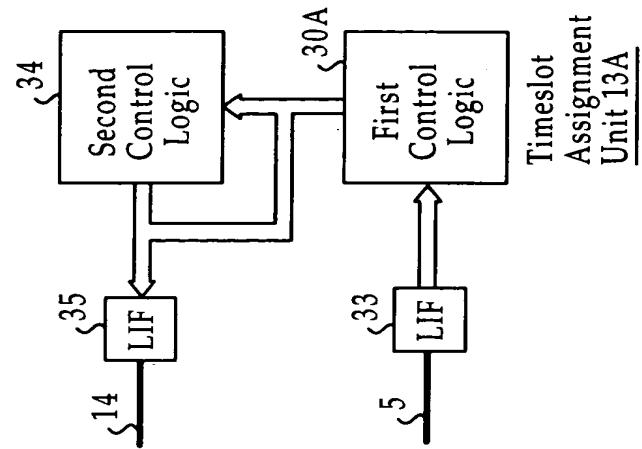


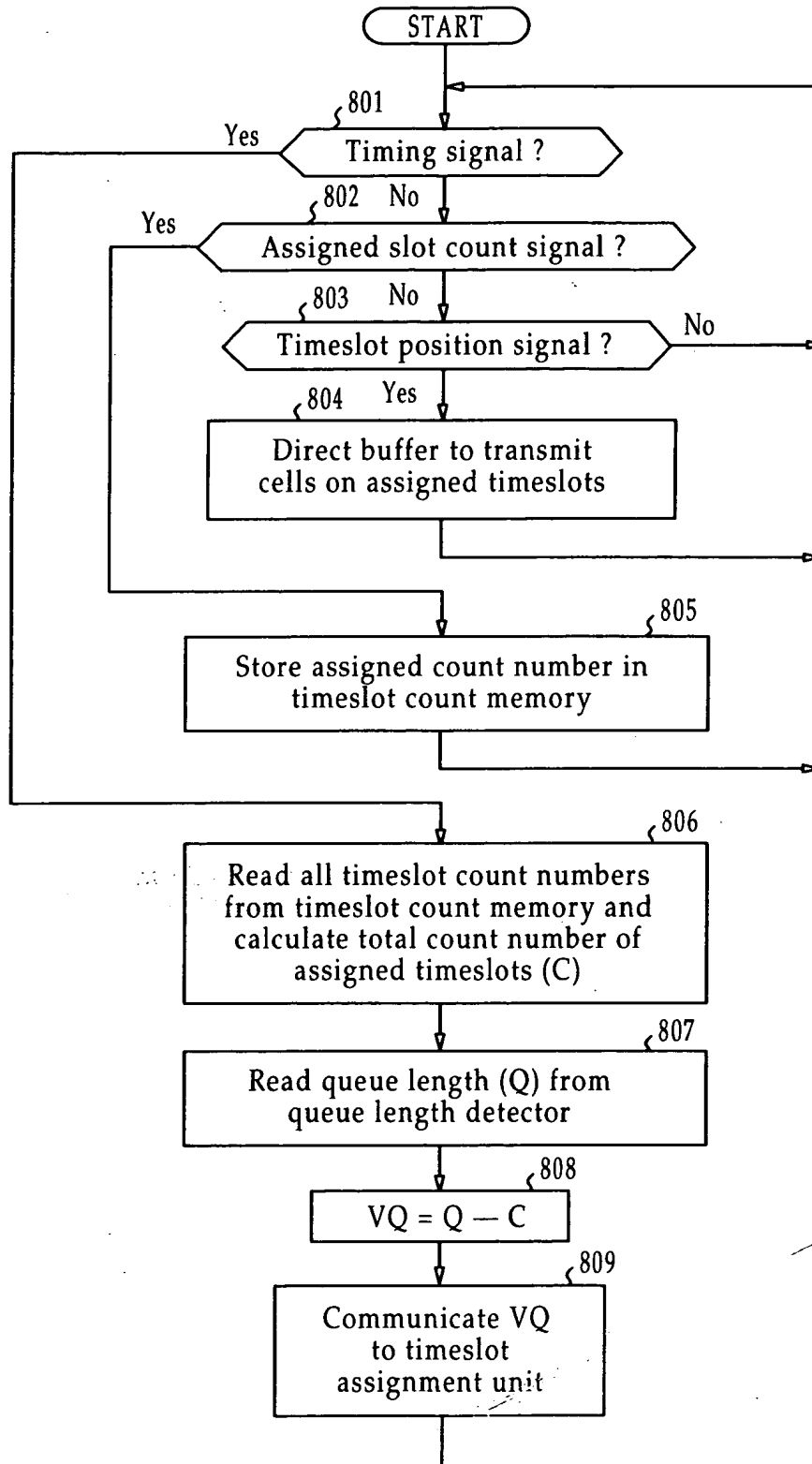
FIG. 12Line Concentrator

FIG. 13First Control Logic 30A